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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: : Date: 12/16/2002
IMAM, MOHAMED et al : Group: 2812
APPLN. NO. 09/917731 : Examiner: Angel Roman
FILED: 7/31/2001 : Tel.: 703 306 0207

FOR: METHOD FOR MANUFACTURING A HIGH VOLTAGE MOSFET SEMICONDUCTOR
DEVICE WITH ENHANCED CHARGE CONTROLLABILITY

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS
BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE
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TO: COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, D.C. 20231
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Date of Deposit
SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC
Name of Assignee

Lydia McNamara
SIGNATURE

12/17/02
DATE

SUBMITTAL OF SUBSTITUTE DECLARATION & POWER OF
ATTORNEY

Honorable Commissioner of Patents and Trademarks,
Washington, D.C. 20231

SIR:

Enclosed is a Substitute Combined Declaration and Power of
Attorney for Patent Application. Please file this document in the
above referenced patent application.

Respectfully submitted,

IMAM, MOHAMED et al

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